

POTS
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q86110

Ichiro HAZEYAMA, et al.

Appln. No.: 10/528,160

Group Art Unit: 2818

Confirmation No.: 6481

Examiner: Earl N. Taylor

Filed: March 17, 2005

For:

SEMICONDUCTOR PACKAGE AND LAMINATED SEMICONDUCTOR PACKAGE

AMENDMENT UNDER 37 C.F.R. § 1.111

MAIL STOP AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 17, 2006, please amend the aboveidentified application as follows on the accompanying pages.

TABLE OF CONTENTS

AMENDMENTS TO THE CLAIMS		2	
REMARKS		7	